

Level of Description	Reference Number	Object Name	Object Summary	Date	Association Type	Association Comments	SubHistoryTechSignificance
Series level	YA2003.112/1	manual	Manuals and handbooks	1949-1970	User	Eric George Lofthouse	13 items relating to computer and radar systems developed by A V Roe and Ferranti Ltd.
Item level	YA2003.112/1/1	manual	Lists of tests and entry points	c.1955	User	Eric George Lofthouse	
Item level	YA2003.112/1/2	manual	A. V. Roe & Co, Digital Computer Programmers Handbook	May-55	User	Eric George Lofthouse	
Item level	YA2003.112/1/3	manual	Electrical and mechanical engineering regulations - Restricted - Equipment, Radar, A. A. No. 4, MK 6, Technical Handbook - Description	30/12/1949	User	Eric George Lofthouse	
Item level	YA2003.112/1/4	manual	Notes on Electrical Control Equipment - BOFORS L70, prepared by A. A. Command School, R.E.M.E.	1955	User	Eric George Lofthouse	
Item level	YA2003.112/1/5	manual	Notes on No. 11 Predictor, prepared by A. A. Command School, R.E.M.E.	1953	User	Eric George Lofthouse	
Item level	YA2003.112/1/6	manual	Notes on Radar A. A. No. 3, Mk 7, prepared by A. A. Command School, R.E.M.E	1954	User	Eric George Lofthouse	
Item level	YA2003.112/1/7	manual	Precis Notes on Control Gear for Equipment 40/70 A. A. L3	Jan-58	User	Eric George Lofthouse	
Item level	YA2003.112/1/8	manual	Ferranti Ltd - Pegasus - summarised programming information	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/1/9	manual	Ferranti Ltd - Orion Computer - summarised programming information	1962	Creator	Ferranti Ltd.	
Item level	YA2003.112/1/10	manual	Anthropometric data; basic visual data; hand measurements of men, women and children; foot measurements and basic foot controls; human strength/body clearances/climbing data/ingress & egress, basic display data	1959-1960	Creator	Henry Dreyfuss - Whitney Publications N. Y. C.	
Item level	YA2003.112/1/11	manual	Basic control data	1960	Creator	Henry Dreyfuss - Whitney Publications N. Y. C.	
Item level	YA2003.112/1/12	manual	ICL Metric conversion	1971	Creator	International Computers Ltd.	
Item level	YA2003.112/1/13	manual	ICL Metrication Guide	Mar-70	Creator	International Computers Ltd.	
Series level	YA2003.112/2	papers	Published papers and manuals	1948-1967			8 items relating to digital computers and component parts.
Item level	YA2003.112/2/1	paper	Universal High-Speed Digital Computers: A Small-Scale Experimental Machine	Feb-51	Creator	The Institution of Electrical Engineers	
Item level	YA2003.112/2/2	paper	Symposium of Papers on Digital Computers	Oct-53	Creator	The Institution of Electrical Engineers	
Item level	YA2003.112/2/3	paper	A Storage System For Use With Binary-Digital Computing Machines	1948	Creator	Prof. F. C. Williams and T. Kilburn	
Item level	YA2003.112/2/4	paper	Mullard Voltage Regulator (Zener) Diodes	May-67	Creator	Mullard Ltd.	
Item level	YA2003.112/2/5	paper	Mullard Valves and Electron Tubes	c.1965	Creator	Mullard Ltd.	
Item level	YA2003.112/2/6	paper	Mullard Reference Manual of Transistor Circuits	1961	Creator	Mullard Ltd.	

Item level	YA2003.112/2/7	paper	Why Pegasus 2 is a Winner	Nov-58	Creator	Ferranti Ltd.	
Item level	YA2003.112/2/8	paper	A Description of the Ferranti Pegasus Computer with Magnetic Tape Equipment	Feb-58	Creator	Ferranti Ltd.	
Series level	YA2003.112/3	drawing	Drawings	c.1955	Creator	Ferranti Ltd.	32 drawings relating to the Ferranti Pegasus and Ferranti Perseus computers, and to the AA Command School radar.
Item level	YA2003.112/3/1	drawing	Circuit diagram - 65/21405/c - External condition control, coincidence circuit, drum transfer controls, pulse delay chain (with inverters), commutator	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/2	drawing	Circuit diagram - 65/21406/c - beat trigger D-staticiser, beat counter E-staticiser, Start-Normal-Manual switch, order register, timing controls, modifier controls, order number register, order adder, n-bus and party circuit, order register delay chain	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/3	drawing	Pegasus 1 - Layout showing 1. package type 2. grid references to diagram 65/21401-8 3. output waveform numbers 4. register addresses - 65/21409	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/4	drawing	Circuit diagram - 65/26799 SHT 1 - controls for multiplier divider, multi-beat timing controls, mail connections functions 20-23 acc 6 adder subtractor control, function 37 zero suppression register, multiplicand divisor register, accumulator 7, accumulator 6	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/5	drawing	Circuit diagram - 65/26799 SHT 2 - n-decoding controls, handkey dynamiciser, inout bus gating and party circuit, constants, n-decoding, half adder for block transfers, accumulator 1-5, x/m decoding	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/6	drawing	Circuit diagram - 65/26799 SHT 3 - "erase" signal, ordinary registers, function 76	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/7	drawing	Circuit diagram - 65/26799 SHT 4 - function decoding: f&g waveforms, x-bus and party circuit, m-bus, mill adder-subtractor controls, buffer input, mill entries, stops, mill adder-subtractor, jump condition testing, overflow testing, shift count	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/8	drawing	commutator, drum transfer controls, pulse delay chain (with inverters), phase-splitting logic, stack selection staticisers, block selection staticisers	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/9	drawing	Circuit diagram - 65/26799 SHT 6 - beat trigger, d-staticiser, beat counter, e-staticiser, start-normal-manual switch, order register timing controls, modifier controls, order number register, order adder, n-bus and party circuit, order register delay chain, 25-place shift	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/10	drawing	Circuit diagram - 65/26799 - SHT 7 - track decoding, block decoding, first track, second track	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/11	drawing	paper tape signal dynamiciser and party circuit, multibeat counter (monitor trigger), input/output control, dynamicisers for monitor handkeys, block address punching, coincidence circuit (monitor), punch digit staticisers, beat selection (trigger) control signals to monitor unit	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/12	drawing	65/26799 SHT 9 - Layout showing 1. package types 2. grid references 3. output waveform numbers 4. register addresses	c.1955	Creator	Ferranti Ltd.	

Item level	YA2003.112/3/13	drawing	Circuit diagram - 65/59087 - electro data unit pegasus (this drawing to be used in conjunction with 65/76361)	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/14	drawing	Circuit diagram - D65/73502 SHEET 1 OF 2 - I02 computer - Power unit	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/15	drawing	Circuit diagram of electrodata tape unit for Perseus	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/16	drawing	Modifications to magnetic tape control for Pegasus II	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/17	drawing	Fig. 1.1, modifications to main computer	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/18	drawing	Fig. 1.2, more modifications to main computer	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/19	drawing	Fig. 1.3, other cathode followers	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/20	drawing	Fig. 1.4, P-pulse generator	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/21	drawing	Fig. 2.1, computer, interchange, tape store	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/22	drawing	Fig. 2.2, buffer store, section 2	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/23	drawing	Fig. 2.3, buffer store, section 0	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/24	drawing	Fig. 2.4, buffer entries and outputs	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/25	drawing	Fig. 2.5, buffer controls	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/26	drawing	Fig. 2.6, tape order register, error register, and automatic repeat	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/27	drawing	Fig. 2.7, tape order decoding	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/28	drawing	Fig. 2.8, mechanism selection	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/29	drawing	Fig. 3.1, address - read circuit	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/30	drawing	Fig. 4.3, drum electronics (writing)	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/31	drawing	Fig. 5.4, gating controls for read staticisers	c.1955	Creator	Ferranti Ltd.	
Item level	YA2003.112/3/32	drawing	Notes on radar AA No 4 Mks 6 & 6/2 - drawings	Oct-53	Creator	Prepared by AA Command school, R. E. M. E.	
Series level	YA2003.112/4	photographs	Photographs of machines	c.1965	Creator	Ferranti Ltd.	8 items relating to the Ferranti Computer Department at West Gorton.
Item level	YA2003.112/4/1	photograph	View of 1900 system assembly & commissioning, West Gorton	1965	Creator	Ferranti Ltd.	
Item level	YA2003.112/4/2	photograph	View of 1900 system assembly & commissioning, West Gorton, Bob Monks, Grant Emerson	c.1965	Creator	Ferranti Ltd.	
Item level	YA2003.112/4/3	photograph	Coincidence checked wiring station - front view	c.1965	Creator	Ferranti Ltd.	
Item level	YA2003.112/4/4	photograph	General view of 1900 computer assembly line, West Gorton	c.1965	Creator	Ferranti Ltd.	
Item level	YA2003.112/4/5	photograph	Two engineers using EGL monitor unit, new range computer, West Gorton	c.1965	Creator	Ferranti Ltd.	
Item level	YA2003.112/4/6	photograph	Coincidence check wiring system, rear view	c.1965	Creator	Ferranti Ltd.	
Item level	YA2003.112/4/7	photograph	No description	c.1965	Creator	Ferranti Ltd.	
Item level	YA2003.112/4/8	photograph	Flat-bed camera used for making 35mm serial monitor film	c.1965	Creator	Ferranti Ltd.	